

PLUS Search Results for S/N 10689553, Searched March 24, 2005

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10689553\_QUAL.txt

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10689553\_CLS.txt  
Most Frequently Occurring Classifications of Patents Returned  
From A Search of 10689553 on March 24, 2005

Original Classifications

7 714/738  
4 714/724  
2 702/183  
2 716/4

Cross-Reference Classifications

4 714/718  
3 714/734  
3 714/736  
3 716/4  
2 257/E21.525  
2 702/185  
2 714/704  
2 714/723  
2 714/733  
2 714/738  
2 716/2  
2 716/5  
2 716/6

Combined Classifications

9 714/738  
5 714/724  
5 716/4  
4 714/718  
3 714/734  
3 714/736  
2 257/E21.525  
2 365/201  
2 438/14  
2 702/183  
2 702/185  
2 714/33  
2 714/704  
2 714/723  
2 714/727  
2 714/733  
2 716/2  
2 716/5  
2 716/6

Titles of Most Frequently Occurring Classifications of Patents Returned  
From A Search of 10689553 on March 24, 2005

- 9 714/738 (7 OR, 2 XR)  
Class 714 : ERROR DETECTION/CORRECTION AND FAULT  
DETECTION/RECOVERY  
714/699 PULSE OR DATA ERROR HANDLING  
714/724 .Digital logic testing  
714/738 ..Including test pattern generator
- 5 714/724 (4 OR, 1 XR)  
Class 714 : ERROR DETECTION/CORRECTION AND FAULT  
DETECTION/RECOVERY  
714/699 PULSE OR DATA ERROR HANDLING  
714/724 .Digital logic testing
- 5 716/4 (2 OR, 3 XR)  
Class 716 : DATA PROCESSING: DESIGN AND ANALYSIS OF  
CIRCUIT OR SEMICONDUCTOR MASK  
716/1 CIRCUIT DESIGN  
716/4 .Testing or evaluating
- 4 714/718 (0 OR, 4 XR)  
Class 714 : ERROR DETECTION/CORRECTION AND FAULT  
DETECTION/RECOVERY  
714/699 PULSE OR DATA ERROR HANDLING  
714/718 .Memory testing
- 3 714/734 (0 OR, 3 XR)  
Class 714 : ERROR DETECTION/CORRECTION AND FAULT  
DETECTION/RECOVERY  
714/699 PULSE OR DATA ERROR HANDLING  
714/724 .Digital logic testing  
714/734 ..Structural (in-circuit test)
- 3 714/736 (0 OR, 3 XR)  
Class 714 : ERROR DETECTION/CORRECTION AND FAULT  
DETECTION/RECOVERY  
714/699 PULSE OR DATA ERROR HANDLING  
714/724 .Digital logic testing  
714/736 ..Device response compared to expected  
fault-free response
- 2 257/E21.525 (0 OR, 2 XR)  
Class 257 : ACTIVE SOLID-STATE DEVICES  
257/E21.515 .....Involving use of mechanical auxiliary part  
without use of alloying or soldering process, e.g.,  
pressure contacts (EPO)  
257/E21.521 .Testing or measuring during manufacture or  
treatment or reliability measurement, i.e., testing of  
parts followed by no processing which modifies parts as  
such (EPO)  
257/E21.525 ..Procedures, i.e., sequence of activities  
consisting of plurality of measurement and correction,  
marking or sorting steps (EPO)
- 2 365/201 (1 OR, 1 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/189.01 READ/WRITE CIRCUIT  
365/201 .Testing
- 2 438/14 (1 OR, 1 XR)

Class 438 : SEMICONDUCTOR DEVICE MANUFACTURING: PROCESS  
438/14 WITH MEASURING OR TESTING

2 702/183 (2 OR, 0 XR)  
Class 702 : DATA PROCESSING: MEASURING, CALIBRATING, OR TESTING  
702/127 MEASUREMENT SYSTEM  
702/182 .Performance or efficiency evaluation  
702/183 ..Diagnostic analysis

2 702/185 (0 OR, 2 XR)  
Class 702 : DATA PROCESSING: MEASURING, CALIBRATING, OR TESTING  
702/127 MEASUREMENT SYSTEM  
702/182 .Performance or efficiency evaluation  
702/183 ..Diagnostic analysis  
702/185 ...Cause or fault identification

2 714/33 (1 OR, 1 XR)  
Class 714 : ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY  
714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING  
714/1 .Reliability and availability  
714/25 ..Fault locating (i.e., diagnosis or testing)  
714/32 ...Particular stimulus creation  
714/33 ....Derived from analysis (e.g., of a specification or by stimulation)

2 714/704 (0 OR, 2 XR)  
Class 714 : ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY  
714/699 PULSE OR DATA ERROR HANDLING  
714/704 .Error count or rate

2 714/723 (0 OR, 2 XR)  
Class 714 : ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY  
714/699 PULSE OR DATA ERROR HANDLING  
714/718 .Memory testing  
714/723 ..Error mapping or logging

2 714/727 (1 OR, 1 XR)  
Class 714 : ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY  
714/699 PULSE OR DATA ERROR HANDLING  
714/724 .Digital logic testing  
714/726 ..Scan path testing (e.g., level sensitive scan design (LSSD))  
714/727 ...Boundary scan

2 714/733 (0 OR, 2 XR)  
Class 714 : ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY  
714/699 PULSE OR DATA ERROR HANDLING  
714/724 .Digital logic testing  
714/733 ..Built-in testing circuit (BILBO)

2 716/2 (0 OR, 2 XR)  
Class 716 : DATA PROCESSING: DESIGN AND ANALYSIS OF CIRCUIT OR SEMICONDUCTOR MASK

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716/1      CIRCUIT DESIGN
716/2      .Optimization (e.g., redundancy, compaction)

2  716/5      (0 OR, 2 XR)
      Class 716 : DATA PROCESSING: DESIGN AND ANALYSIS OF
                  CIRCUIT OR SEMICONDUCTOR MASK
716/1      CIRCUIT DESIGN
716/4      .Testing or evaluating
716/5      ..Design verification (e.g., wiring line
                  capacitance, fan-out checking, minimum path width)

2  716/6      (0 OR, 2 XR)
      Class 716 : DATA PROCESSING: DESIGN AND ANALYSIS OF
                  CIRCUIT OR SEMICONDUCTOR MASK
716/1      CIRCUIT DESIGN
716/4      .Testing or evaluating
716/5      ..Design verification (e.g., wiring line
                  capacitance, fan-out checking, minimum path width)
716/6      ...Timing analysis (e.g., delay time, path
                  delay, latch timing)

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